

LAB 4: OPERATIONAL AMPLIFIER CIRCUITS

Introduction

Operational amplifiers (OAs) are highly stable, high gain, difference amplifiers that can handle signals from zero frequency (dc signals) up to the MHz range. They are used for performing mathematical operations on their input signal(s) in real-time and are an important component of analog computation networks. A large variety of OAs is commercially available in the form of low cost integrated circuits. In these experiments, a commercially available device (like the LM324 or the LM741) OA will be used.

Figure 1 shows the symbol for an OA. There are two inputs, the *inverting input* (-) and the *non-inverting input* (+). These symbols have nothing to do with the polarity of the applied input signals.

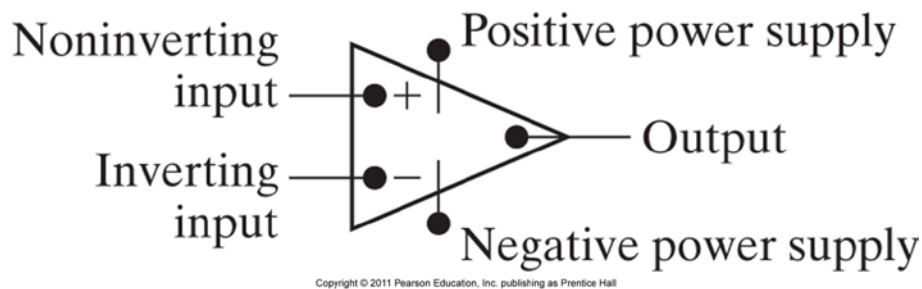


Figure 1 Circuit symbol for the operational amplifier (taken from [1]).

The output signal voltage, v_o , is given by:

$$v_o = A(v_+ - v_-)$$

where v_+ and v_- are the signals applied to the non-inverting and to the inverting input, respectively and A represents the *open loop gain* of the OA. The gain A is infinite for the ideal operational amplifier, whereas for the various types of real OAs, it is usually within the range of 10^6 to 10^8 . The input resistance of both OA inputs is extremely high (usually within the range 10^8 - 10^{12} M Ω). For an ideal OA the input resistance is infinite, thus no current flows into the two OA inputs.

Operational amplifiers, of the type used here, require a bipolar power supply to operate, i.e., a positive voltage (+V) and a negative voltage (-V) with respect to ground. The bipolar power supply allows OAs to generate an output voltage signal, v_o , of either polarity. The output signal range is not unlimited. The voltages of the power supplies determine its actual range. Thus, a typical OA supplied with -15 and +15 V, may yield a v_o approximately within the -13 to +13 V range. This is called the *operational range*. Any result expected to be outside this range is clipped to the respective limit, and the OA is in a *saturation* stage.

Because of their very high open loop gain, OAs are almost exclusively used with some additional circuitry (mostly with resistors and capacitors), required to ensure a *negative feedback loop*. The feedback loop stabilizes the output within the operational range and

provides a much smaller but precisely controlled gain, called the **closed loop gain**. For an ideal OA with feedback, the voltages at the two inputs, v_+ and v_- , are equal.

There are many circuits with OAs performing various mathematical operations. Each circuit is characterized by its own input-output relationship which is the mathematical equation describing the output signal, v_o , as a function of the input signal(s). v_1, v_2, \dots, v_n . Generally, these relations can be derived by applying Kirchhoff's Laws and assuming an ideal OA.

Inverting amplifier

The circuit for an inverting amplifier is shown in Figure 2.

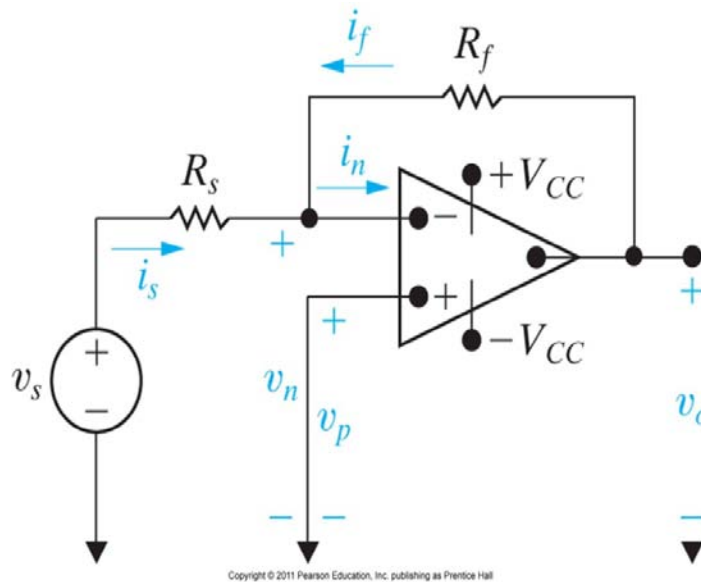


Figure 2 Inverting amplifier circuit (taken from [1]).

1. Derive the input-output relation: $v_o = f(v_s, R_s, R_f)$.
2. For $V_{CC} = 15 \text{ V}$, use $v_s = 1 \text{ V}$, $R_s = 1 \text{ k}\Omega$ and $R_f = 10 \text{ k}\Omega$. Measure v_o and verify that the input-output relation is satisfied.
3. Continue to use $V_{CC} = 15 \text{ V}$, $v_s = 1 \text{ V}$ and $R_s = 1 \text{ k}\Omega$. Choose values for R_f from $11 \text{ k}\Omega$ to $20 \text{ k}\Omega$ (in steps of $1 \text{ k}\Omega$) and measure v_o . Discuss when the input-output relation is satisfied and when clipping occurs. Why does clipping occur for some values of R_f ?

Noninverting amplifier

The circuit for a noninverting amplifier is shown in Figure 3.

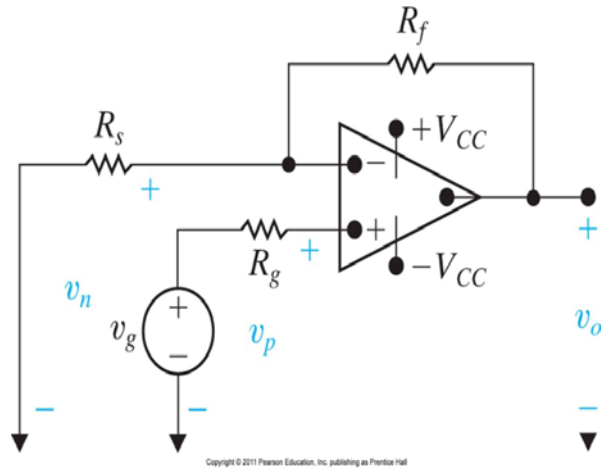


Figure 3 Noninverting amplifier circuit (taken from [1]).

1. Derive the input-output relation: $v_o = f(v_g, R_s, R_f, R_g)$.
2. Use $V_{CC} = 15 \text{ V}$, $v_g = 1 \text{ V}$ and $R_g = 1 \text{ k}\Omega$. Design a noninverting amplifier (choose values of R_s and R_f) with an output voltage of $v_o = 3 \text{ V}$ such that the power dissipated in R_s and R_f is less than or equal to 0.003 W . Measure v_o and verify that the input-output relation is satisfied. Show all your calculations.

Ideal Differentiator

The differentiator generates an output signal proportional to the first derivative of the input with respect to time. The circuit is shown in Figure 4.

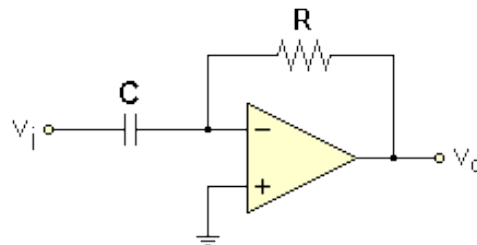


Figure 4 Differentiator circuit

The input-output relation of this circuit is

$$v_o = -(RC) \frac{dv_i}{dt}$$

Derive the input-output relation given above and explain why any input noise is amplified at *high* frequencies.

Ideal Integrator

The integrator generates an output signal proportional to the time integral of the input signal. The circuit is shown in Figure 5.

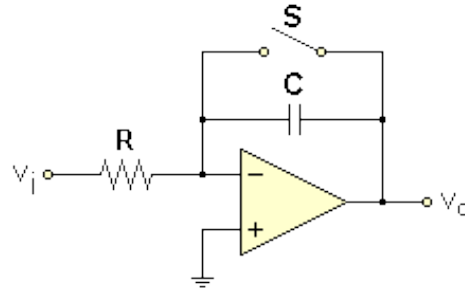


Figure 5 Integrator circuit

The input-output relation of this circuit is

$$v_o = -\frac{1}{RC} \int v_i(t) dt$$

The output remains zero when the switch S remains closed. The integration starts ($t = 0$) when S opens.

Derive the input-output relation given above, and explain why input signals with *low* frequencies are amplified.

Practical Differentiator

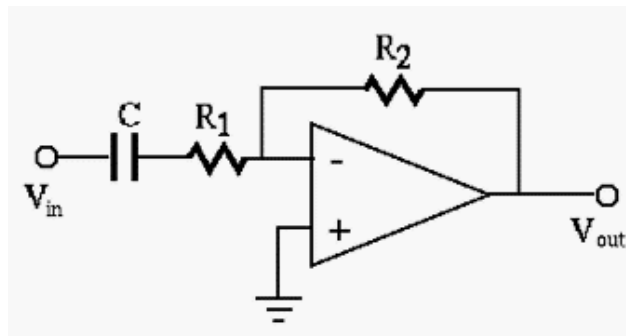


Figure 6 Practical Differentiator circuit

To mitigate the problem of noise amplification at high frequencies, a differentiator used in practice is given in Figure 6. Differentiation of the input signal is accomplished over a bandwidth of low frequencies.

1. Derive the input-output relation for the circuit of Figure 6. This can be done in the Laplace transform domain. **[We will do this later in ELEC 225/226.]**
2. For $R_1 = 1.6 \text{ k}\Omega$, $R_2 = 100 \text{ k}\Omega$ and $C = 0.01 \text{ }\mu\text{F}$, run the following MATLAB program to plot the magnitude response of the circuit.

```
n=[ -r2*c  0 ];  
d=[r1*c  1];  
[h,w]=freqs(n,d);  
h=abs(h);  
f=w/(2*pi);  
plot(f,h); xlabel('Frequency (Hz)'); ylabel('Gain');
```

 - Explain the MATLAB code. Submit all plots.
 - From the plot, deduce the bandwidth of frequencies for which differentiation is performed. (Where is the slope *linear*?)
 - For high frequencies, the circuit reduces to an inverting amplifier with absolute gain R_2/R_1 . After about what frequency does this occur (deduce from the plot)?
3. Using the component values given above, build the circuit. Observe and explain the input and output waveforms on the oscilloscope. Use the following inputs:
 - A square wave (500 Hz fundamental) with a peak to peak voltage of 100 mV.
 - A triangular wave (500 Hz fundamental) with a peak to peak voltage of 100 mV.
 - A 500 Hz sine wave with a peak to peak voltage of 100 mV. Increase the frequency of the sine wave to 1000 Hz, 1500 Hz, 2000 Hz, 3000 Hz, 5000 Hz, 10000 Hz, 20000 Hz, 30000 Hz and 40000 Hz. Explain what you observe.

Lab Report Format (Each lab group should submit one report.)

1. Title Page
2. Table of Contents
3. Abstract
4. Introduction and Objectives of Laboratory Experiment
5. Background Material on Operational Amplifiers
6. Results and Discussion. Include the answers to the questions given above.
7. Conclusions
8. References
9. Appendices

References

1. J. W. Nilsson and S. A. Riedel, Electric Circuits, 9th edition, Prentice-Hall, 2011.